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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Appln. Ser. No.:	Filed:	Inventor(s):	Atty Dkt:
09/826,383	4 April 2001	S. Yoshida	114GI-143 (0694-143)
Title: Electromagnetic Noise Suppressor, Semiconductor Device Using the Same, and Method of Manufacturing the Same			
Examiner: Khiem Nguyen		Art Unit: 2823	

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Comm'r for Patents
PO Box 1450
Alexandria, VA 22313-1450

10 pages VIA FACSIMILE
571-273-8300

SECOND RESPONSE AFTER FINAL REJECTION

Dear Sir:

In complete response to the action mailed 1 June 2005 and to the Advisory Action mailed 09/28/2005, please first amend the claims as shown on the following eight pages, after which are remarks traversing the rejections.

Respectfully submitted,

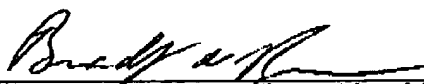
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I hereby certify that I have a reasonable basis that this paper, along with any referred to above, (i) are being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450, or (ii) are being transmitted to the U.S. Patent & Trademark Office in accordance with 37 CFR § 1.6(d)

DATE: 18 October 2005

NAME: Brad Ruben

SIGNATURE: Brad Ruben


Bradley N. Ruben, Reg. No. 32,058
Bradley N. Ruben, PC
463 First St., Suite 5A
Hoboken, NJ 07030-1859
201-239-0707 (fax -0734)
mail@rubenpatent.com

18 October 2005

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